

Appl. No. 10/773,445

**In the Specification**

Please amend the specification as set forth below:

Please amend the paragraph beginning at page 12, line 1 as follows:

Simultaneously, the logic processing unit 24 activates, at step 54, the timer 42 and permanently reads, during step [[58]] 56, the data transmitted by the acquisition circuit 22 and the voltage  $V_{GE}$ .